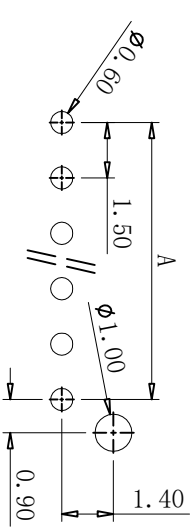
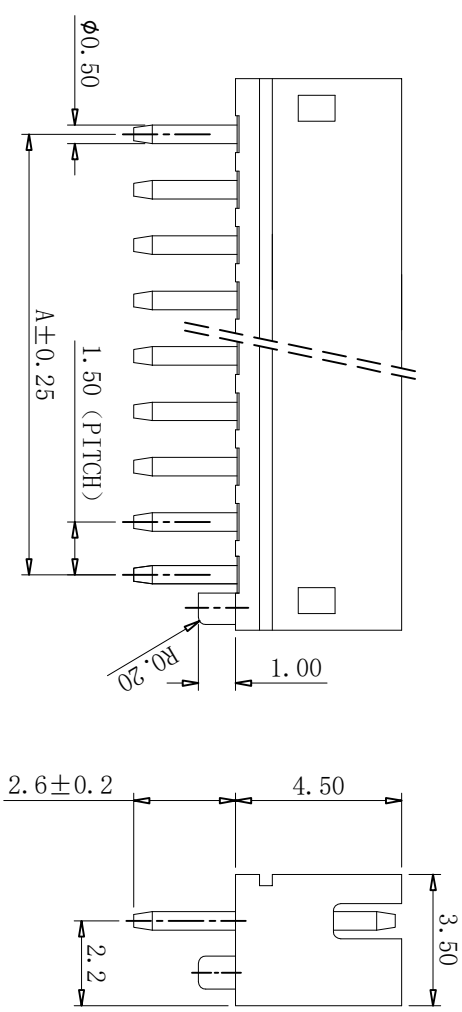


REV.	ECN NO	DESCRIPTION	REVISED	DATE
A		新订图面	KEVIN	2015.03.10

**ROHS**



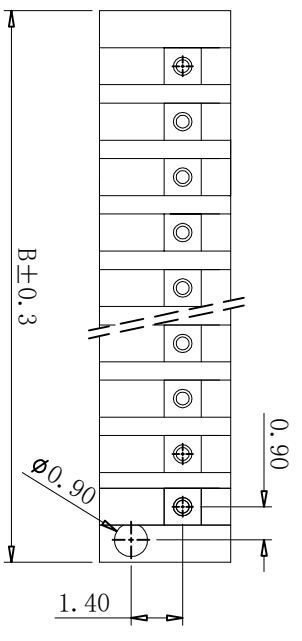
Recommended P.C. Board Hole Layout


CKT.	A	B
2	1.50	4.50
3	3.00	6.00
4	4.50	7.50
5	6.00	9.00
6	7.50	10.50
7	9.00	12.00
8	10.50	13.50
9	12.00	15.00
10	13.50	16.50
11	15.00	18.00
12	16.50	19.50
13	18.00	21.00
14	19.50	22.50
15	21.00	24.00
16	22.50	25.50

**Specifications / 技术规格:**

Current rating: 1A  
 Voltage rating: 50V AC, DC  
 Insulation Resistance: 500MΩ Min.  
 Withstanding Voltage: 500V AC 1分钟  
 Contact Resistance: 20mΩ Max.  
 Operating Temperature: -25°C to +85°C  
 工作温度: -25° C 到85° C

**Materials / 材料:**  
 Contact Material: Brass(plating Tin)  
 接触端子材料: 黄铜(镀锡)  
 Insulation Material: PA or PBT UL94V-0 Natural  
 绝缘体塑胶材质: PA 或 PBT UL94V-0 本色




**浙江快利电子有限公司**  
 Zhejiang Kuaili Electronic Co., Ltd

GENERAL TOLERANCE		DESIGN: KEVIN	
X' ± 3'	.XXX	CHECKED:	DATE: 2015-03-10
X' ± 2'	.XX	AUDITED:	DATE:
.XX' ± 1'	.X	APPROVED:	DATE:
.XXX' ± 0.5'	X		

DWG. NO.		TITLE
PART NO.	KL-ZH-NAD	ZH-NAD

A B C D E F

1 2 3 4 5 6 7 8 9

A B C D E F