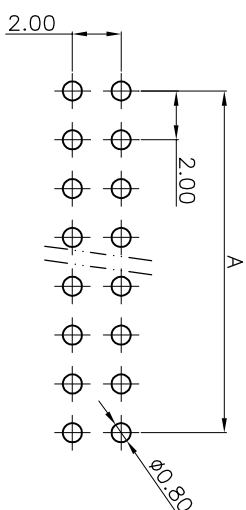


PIN数	A	B
2×2	2.00	6.00
2×3	4.00	8.00
2×4	6.00	10.00
2×5	8.00	12.00
2×6	10.00	14.00
2×7	12.00	16.00
2×8	14.00	18.00
2×9	16.00	20.00
2×10	18.00	22.00
2×11	20.00	24.00
2×12	22.00	26.00
2×13	24.00	28.00
2×14	26.00	30.00
2×15	28.00	32.00
2×16	30.00	34.00
2×17	32.00	36.00
2×18	34.00	38.00
2×19	36.00	40.00
2×20	38.00	42.00



Recommended P.C.Board Hole Layout

Specifications / 技术规格:

- Current Rating: 3A
- 额定电流: 3A
- Voltage rating: 250V AC,DC
- 额定电压: 250V AC, DC
- Insulation Resistance: 1000MΩ Min.
- 绝缘电阻: 最小1000MΩ
- Dielectric Withstanding: 800V AC
- 耐电压: 800V AC
- Contact Resistance: 20mΩ Max.
- 接触电阻: 最大20mΩ
- Operating Temperature: -25°C to +85°C
- 工作温度: -25° C 到 85° C
- Materials / 材料:
- Contact Material: Brass(Tin plating)
- 接触端子材料: 黄铜(镀锡)
- Insulation Material: PA or PBT UL94V-0 Natural
- 绝缘体塑胶材料: PA 或 PBT UL94V-0 本色

REV.	ECN NO	DESCRIPTION	REVISED	DATE
A		新订图面	HELLER	2016.6.11
A1	20210526047	修改2X3结构	HELLER	2021.5.26

**浙江快利电子有限公司**  
Zhejiang Kuaili Electronic Co., LTD

REV. A1 UNIT. mm SCALE. - PROJ.

GENERAL TOLERANCE

X: ±3°	X: -
X.X: ±0.30	X.XX: ±0.20
.XX: ±1°	X.XXX: -
.XXX: ±0.5°	X.XXX: -

DESIGN: 瞿益远

CHECKED: 朱建慧

APPROVED: 朱建华

DATE: 2021-05-26

DATE: 2021-05-26

DATE: 2021-05-26

DWG. NO.

PART NO.

KL-PHD-2XNAW

TITLE:

PHD-2XNAW